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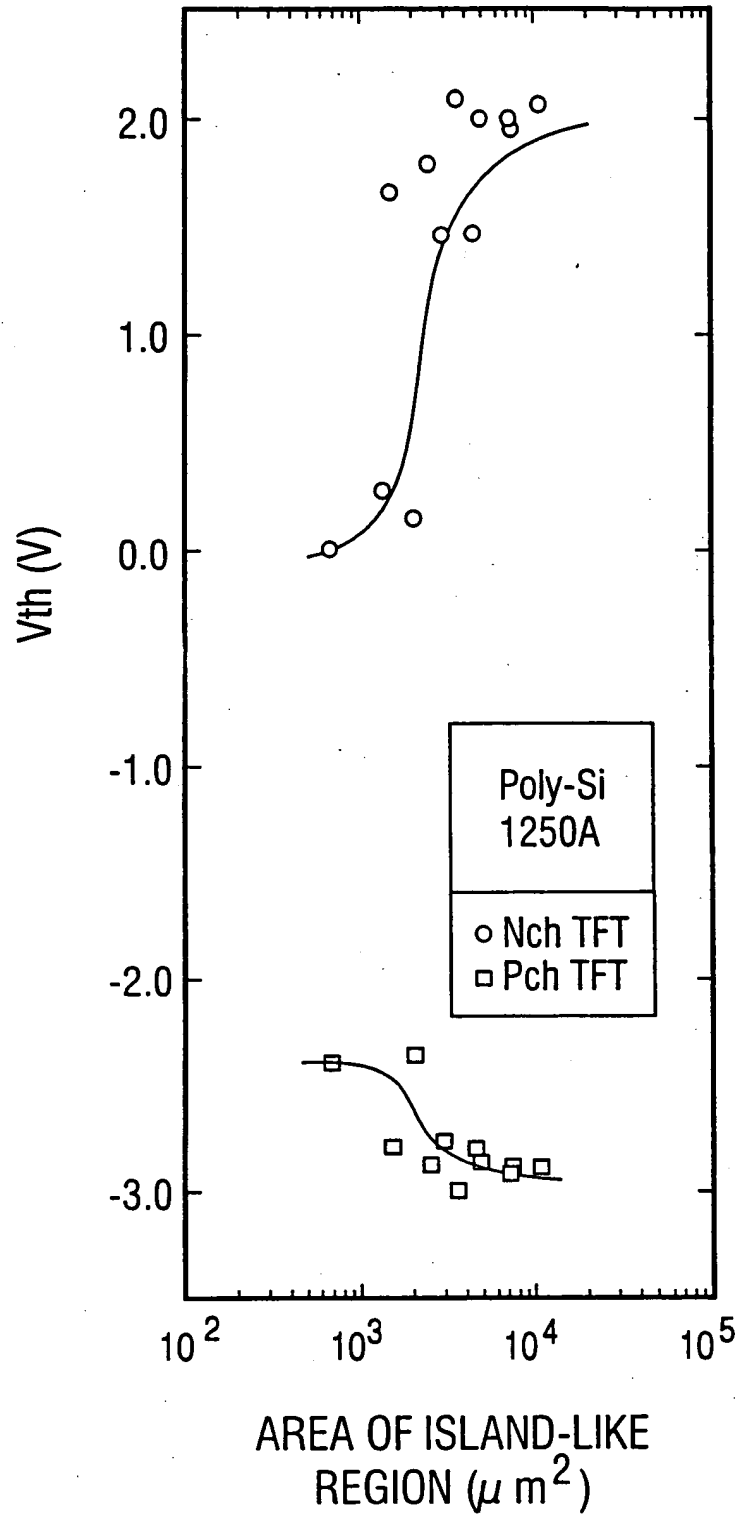
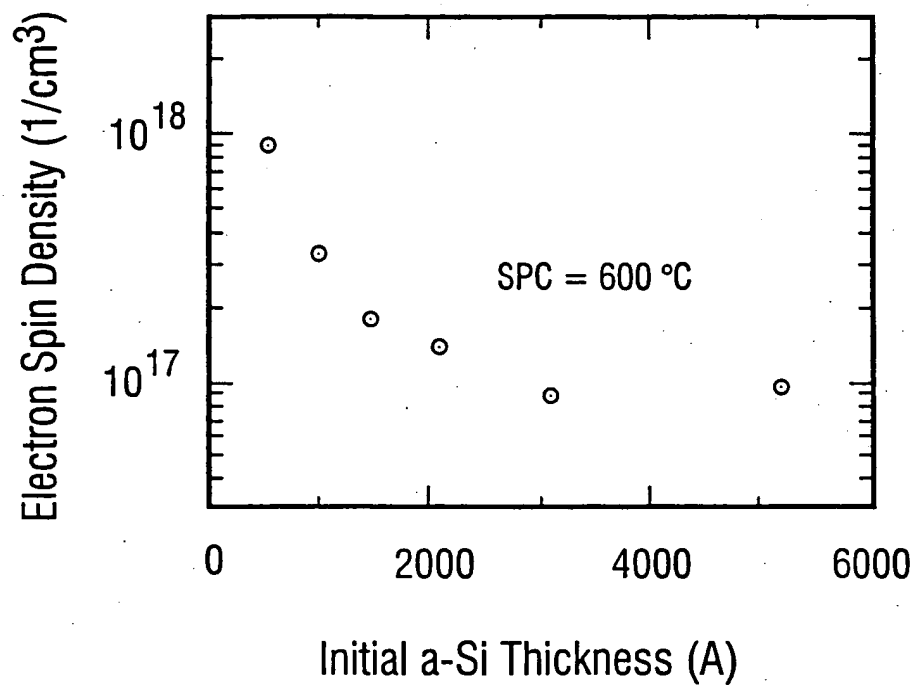


Figure 1

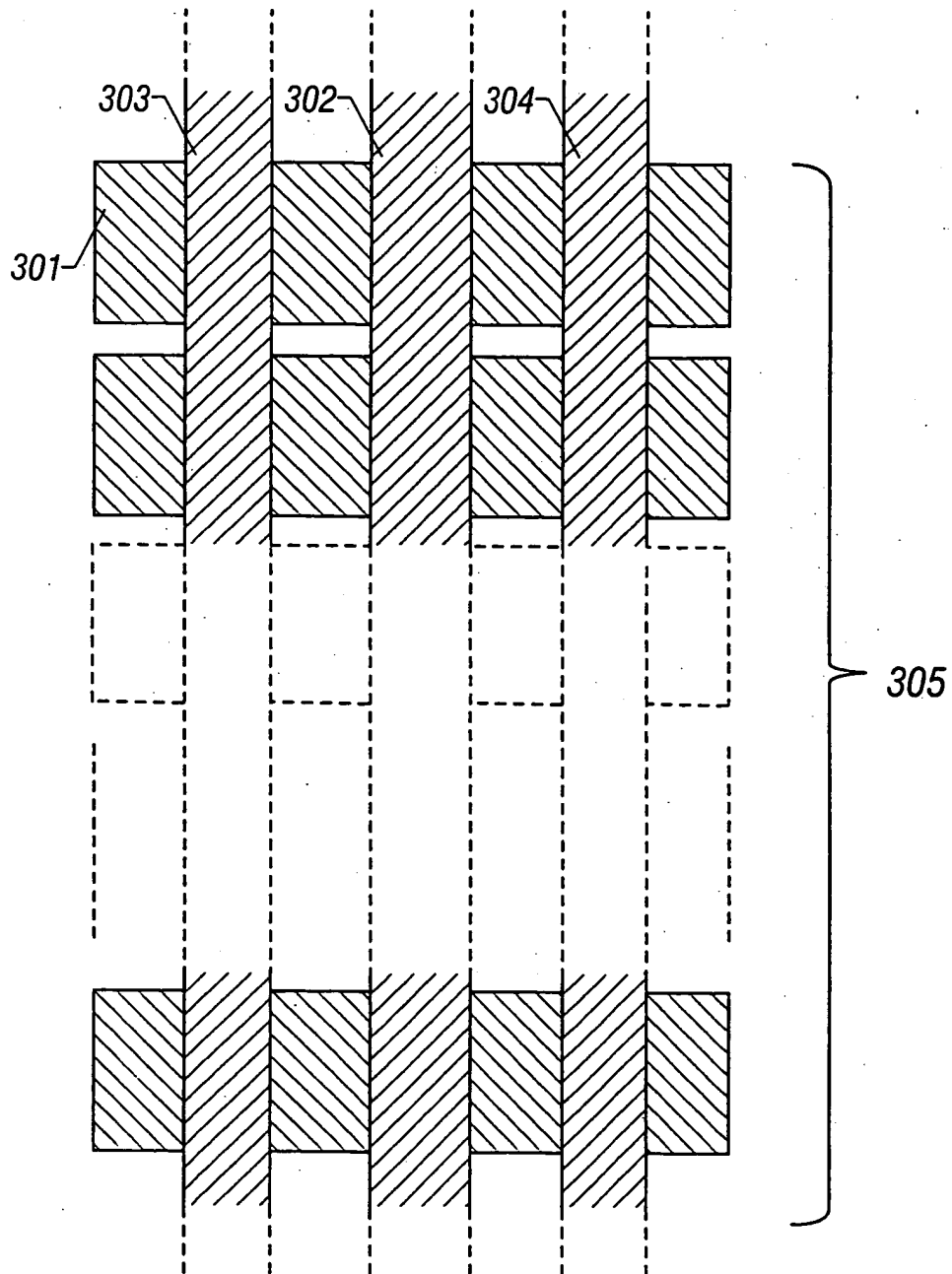
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**Figure 2**

Applicant(s): Isamu Kobori et al.

METHOD OF MANUFACTURING A SEMICONDUCTOR
METHOD OF MANUFACTURING A THIN-FILM TRANSISTOR
AND THIN-FILM TRANSISTOR

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**Figure 3**

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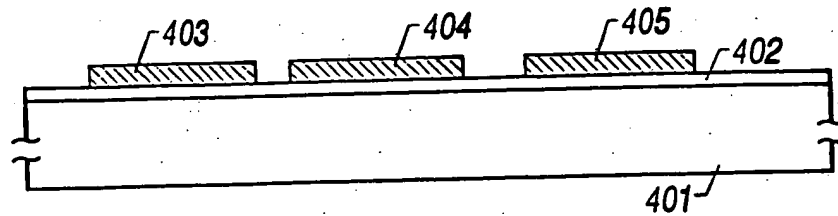


Figure 4A

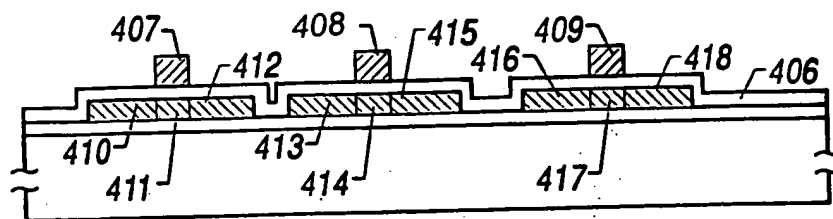


Figure 4B

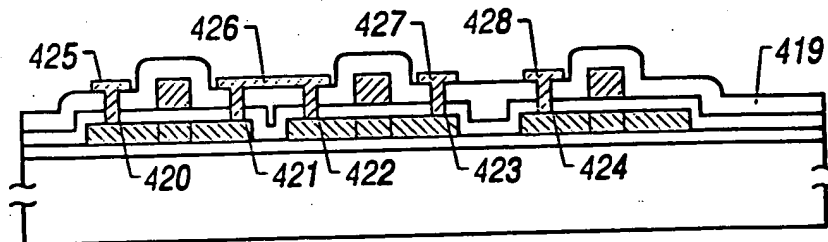


Figure 4C

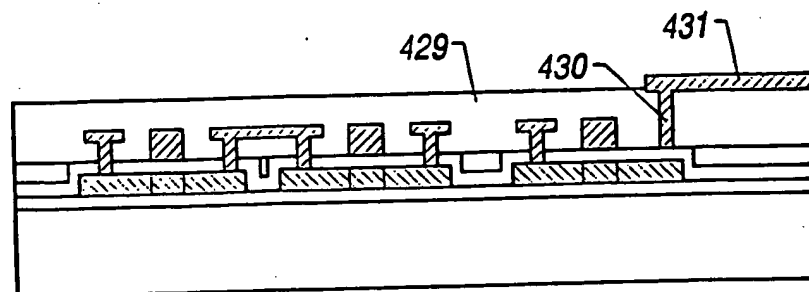


Figure 4D

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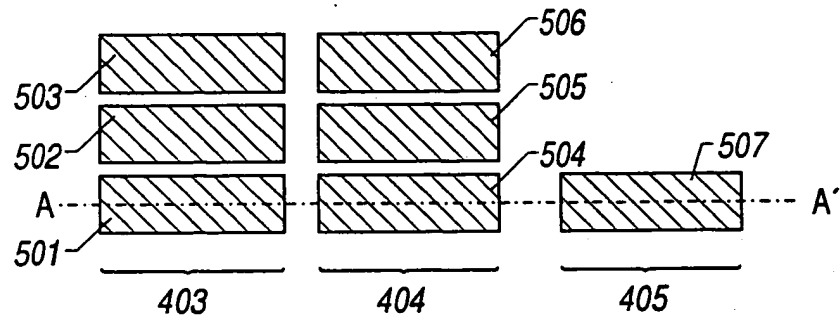


Figure 5A

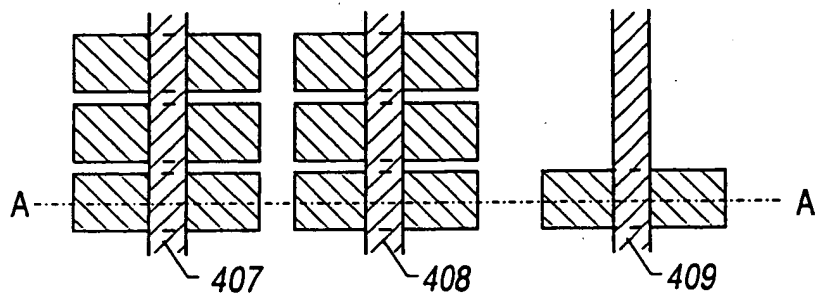


Figure 5B

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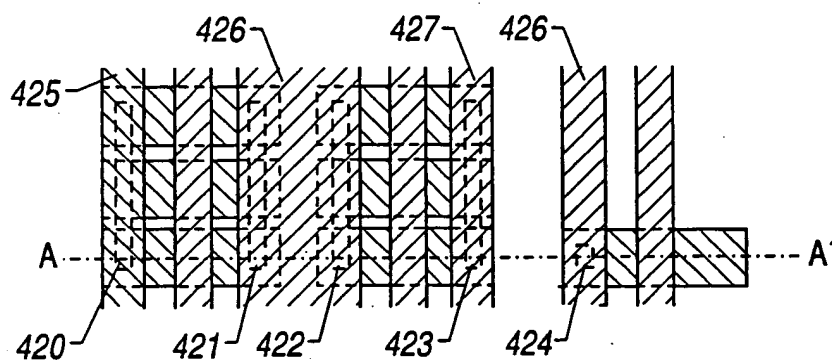


Figure 5C

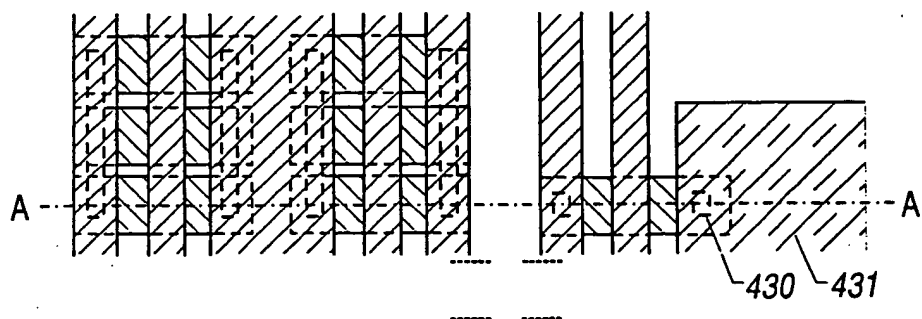


Figure 5D